

**Bhoj Reddy Engineering College for Women
Department of Electronics & Communication Engineering**

Time Table for the Academic Year 2015-16

II B.Tech I Semester ECE Section-C

Room No : 404 North Block (NB)

w.e.f: 27-07-2015

Day	930-1030	1030-1130	1130 - 1230	1230 – 1330	1330 - 1430	1430-1530	1530-1630
Monday	EDC/BS LAB			LUNCH	SS	PTSP(G1-404) M-III(G2-403) EDC(G3-411)	M -III
Tuesday	STLD	SS(G1-404) STLD(G2-403) EC(G3-411)	EDC		BS/EDC LAB		
Wednesday	M -III	STLD	SS		EDC(G1-404) EC(G2-403) M-III(G3-411)	PTSP	EC
Thursday	PTSP	EC	EC(G1-404) SS(G2-403) STLD(G3-411)		M -III	EDC	STLD
Friday	EC	M -III	EDC		STLD(G1-404) PTSP(G2-301) SS(G3-302)	SS	PTSP
Saturday	SS	M-III(G1-404) EDC(G2-403) PTSP(G3-411)	PTSP		STLD	EC	EDC

S.No	Subject		Faculty	Dept
1	M III	Mathematics -III	M.Gnana Prasuna	Maths
2	PTSP	Probability Theory & Stochastic Processes	G.Veena	ECE
3	STLD	Switching Theory & Logic design	S.Manjula	ECE
4	EC	Electrical Circuits	G.Karunakar Reddy	EEE
5	EDC	Electronic devices & circuits	Nasreen Sultana	ECE
6	SS	Signals & Systems	S.Surekha	ECE
7	EDC LAB	Electronic devices & circuits Lab	A.Navila, M.Swapna/ Nasreen Sultana	ECE
8	BS LAB	Basic Simulation Lab	S.Surekha, Neha Jain/K.Anjaneyulu	ECE

Tutorial Groups :

G1: 4C1-4E4

G2: 4E5-G8

G3: 4G9- last Number

Incharge
B.Prabhavathi/S.Deepika

HOD
Dr.K.Yedukondalu

Principal
Dr.G.Ravinder Reddy